



A MAXIMUM external current is 25mA for the sum of all GPIO currents and the current being drawn from VDD **VIN** 5-21 V input to the board.

NOTE: CIPO/COPI have previously been referred to as MISO/MOSI

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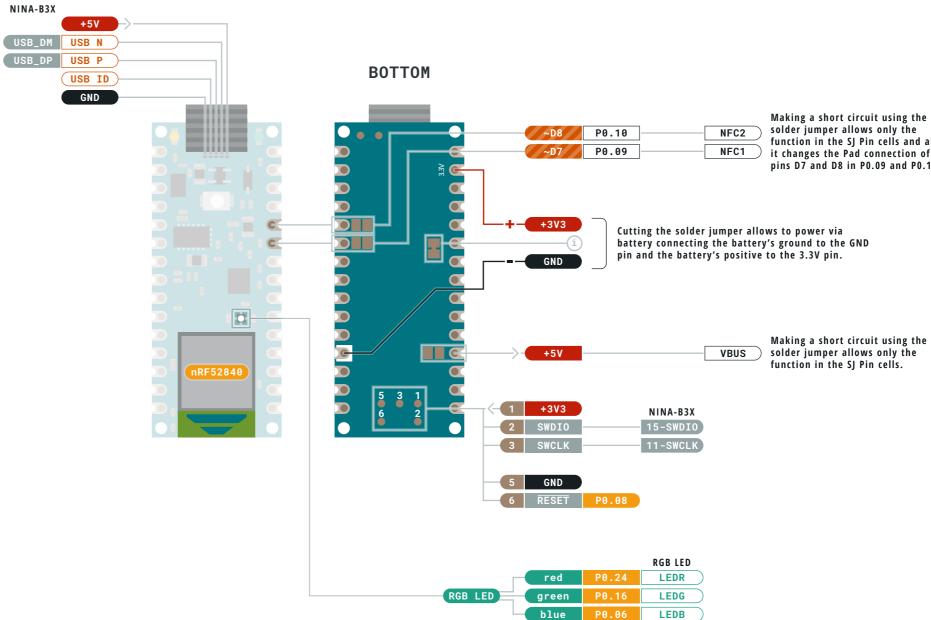
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function in the SJ Pin cells and also it changes the Pad connection of the pins D7 and D8 in P0.09 and P0.10.

	RGB LED
4	LEDR
6	LEDG
6	LEDB



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